

## **Texas Instruments**

MAJORS: Chemical Engineering, Computer Engineering, Computer Science, Electrical Engineering, Engineering Mechanics/Engineering Physics, Materials Science, Mechanical Engineering, Physics

TITLE: Applications Engineering Intern & Applications Engineer

### JOB DESCRIPTION:

Applications Engineering Interns play an important role at Texas Instruments which encompasses the following responsibilities and skills:

- Design new semiconductor products, support silicon evaluation reviews and customers through the complete design-in process.
- Understand market conditions in order to drive critical business decisions about new products and targeted end applications.
- Train field application engineers, distributors, and customers in technical knowledge.
- Thrive in a fast paced and dynamic environment where high-energy, prioritization skills and adaptability are a must!

Life at TI as an Applications Engineering Intern: Enjoy an environment of smart collaboration with a global team of the industry's top engineering minds at your disposal. TI's corporate culture is one of competitive spirit which fuels continued business growth and constant career opportunities. While ethical business practice is a central aspect of the work place- you will be treated with respect, dignity, kindness and courtesy.

Impact people's lives: Create the products and technologies that will go into end products such as desktop PCs, MP3 players, blood pressure monitors, cameras, DVR & DVS', e-book, ECG electrocardiogram, ultrasound system, tablets, automotive and so many more- The possibilities are literally endless!

### Qualifications

- Degree Type: Pursuing BS or MS EE or ECE
- GPA: 3.0/4.0 (minimum)
- Strong understanding of analog, embedded, and/or mixed-signal circuits
- Demonstrated problem solving and communication skills

### Additional Preferred Qualifications

- Proficiency with bench level test equipment (DC power supplies, oscilloscopes, multi-meters)
- Background in circuit test, data analysis and troubleshooting
- Experience with Verilog, in addition to VHDL
- A thorough understanding of both IC & system applications

In order to be eligible for this position you must:

- Visit [www.careers.ti.com](http://www.careers.ti.com)
- Create a new account
- Upload your resume
- Update your profile (enter your contact information, work experience and education)
- And apply to Job ID: 688BR

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Role: Applications Engineer

Applications Engineers play a vital role at Texas Instruments which encompasses the following responsibilities and skills:

- Design new semiconductor products, support silicon evaluation reviews and customers through the complete design-in process.
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#### Qualifications

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And apply to Job ID: 689BR

#### TITLE: Design Engineering Intern & Design Engineer

Design Engineering Interns play an important role at Texas Instruments which encompasses the following responsibilities and skills:

- Manage the design, layout and characterization of ICs for numerous applications.
- Design complete and innovative IC solutions; Bench test and characterize new designs, drive design reviews, and necessary product documentation
- Partner with the businesses to build customer relationships and to support their needs, enabling competitive paced time-to-market.
- Supervise the IC layout by supporting product qualification and production, ensuring reliability and yield requirements, design testability, and test coverage.
- Thrive in a fast paced and dynamic environment where high-energy, prioritization skills and adaptability are a must!

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Qualifications:

- Degree Type: Pursuing BS or MS EE or ECE
- GPA: 3.0/4.0 (minimum)
- Strong understanding of analog, embedded, and/or mixed-signal circuits
- Demonstrated problem solving and communication skills

Additional Preferred Qualifications:

- Proficiency with bench level test equipment (DC power supplies, oscilloscopes, multi-meters)
- Background in circuit test, data analysis and troubleshooting
- Experience with Verilog, in addition to VHDL

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Role: Design Engineer

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#### Qualifications:

- Degree Type: BS or MS EE or ECE
- GPA: 3.0/4.0 (minimum)
- Strong understanding of analog, embedded, and/or mixed-signal circuits
- Demonstrated problem solving and communication skills

#### Additional Preferred Qualifications:

- Proficiency with bench level test equipment (DC power supplies, oscilloscopes, multi-meters)
- Background in circuit test, data analysis and troubleshooting
- Experience with Verilog, in addition to VHDL

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## TITLE: Semiconductor Process Engineer Intern

Semiconductor process engineering continues to push the technology envelope and this is an excellent opportunity to learn and gain experience with people working on the cutting edge of the semiconductor industry. Selected Interns / Co-ops are given work assignment and projects that are meaningful, challenging, and provide an educational experience for the student while they contribute to the success of TI.

Exact intern projects are determined at the beginning of the work term based on current priorities and the strengths of the selected student. Process engineering efforts will revolve around optimization efforts to achieve fab cycle time goals. Intern / Co-Op projects will involve identifying, developing, characterizing, and improving processes to meet program requirements for newest generation devices. Students are often involved with the development of new or modified process formulations, definition of processing or handling equipment requirements and specifications, review of processing techniques and methods applied in the manufacture, fabrication and evaluation of semiconductors. Interns / Co-Ops often review product requirements with staff to ensure compatibility of processing methods. Some projects involve recommending and preparing changes, additions, and modifications which will facilitate manufacturing. Students may have the opportunity to compile and evaluate test data to determine appropriate limits and variables for process or material specifications. May also conceive and plan projects involving definition and selection of new concepts and approaches in the processing or development of new or improved processes in photomasking, diffusion, deposition, wafer fabrication, and device physics. Follows standard practices in analyzing situations or data from which answers can be readily obtained. Students may be involved with process characterization and integration. These projects could support integration teams in addressing yield and parametric issues or efforts to prototype and evaluate new devices and designs.

Some student projects also involve process ownership, including lot disposition, statistical process control, engineering changes and tool releases.

Target Majors: Chemical Engineering, Materials Science, Mechanical Engineering, Electrical Engineering, Microelectronics or Physics

Some of the benefits of a Co-Op with Texas Instruments

- Competitive salary
- Paid relocation - to qualified students to and from any TI site
- Benefits continuation - between work terms, students are placed on educational leave of absence.

Students continue to accrue service time while finishing their degree.

- Professional development opportunities

TI Co-Op Program Requirements

- Minimum 3.0/4.0 GPA overall for new and returning students
- Pursuing an undergraduate or graduate degree in Chemical Engineering, Materials Science, Mechanical Engineering, Electrical Engineering, Microelectronics or Physics

- Demonstrated problem solving and communication skills
- Values & philosophies consistent with the ethics and business philosophy of TI

Don't let this opportunity pass you by!

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- And apply to Job ID: 703BR

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### Semiconductor Fab Process Engineer

Semiconductor process engineering continues to push the technology envelope. This entry-level position provides an outstanding opportunity to be challenged while being able to have an impact in driving for solutions in the module as an individual contributor and team leader. Selected candidates will gain experience with the people working on the cutting edge of the semiconductor industry.

Semiconductor Fab Process Engineers interface across modules and with Yield, Product, and Integration organizations to resolve issues limiting yield performance and are responsible for engineering tasks supporting process engineering including project management to insure timely execution of actions to resolve issues. Semiconductor Fab Process Engineers may also be involved with significant projects including equipment automation, installation, and maintenance. Fab Engineers often review product requirements ensure compatibility of processing methods. Some projects involve recommending and preparing changes, additions, and modifications which will facilitate manufacturing. Engineers also sometimes compile and evaluate data to determine appropriate limits and variables for process or material specifications. Some projects also involve process ownership, including lot disposition, engineering changes and tool releases.

In this role, you will be responsible for process engineering optimization efforts to achieve fab cycle time goals. Specific responsibilities include:

- Support teams addressing module projects.
- Provide progress reports for tools, processes, and goals.
- Track progress on projects and meet milestone dates to enable on-time completion.

- Interface across modules and with yield, product, and integration organizations to resolve issues limiting yield performance.

The ideal candidate will have a degree in Chemical Engineering, Materials Science, Physics or Electrical Engineering. This engineer must be able to complete engineering tasks that include project management to ensure timely execution of actions that resolve issues.

The ability to manage work assignments independently and many tasks at once is a must. This includes the ability to identify and manage priorities.

Don't let this opportunity pass you by!

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- And apply to Job ID: 711BR

#### TITLE: Software Engineering Intern

Software Engineering Interns play an important role at Texas Instruments which encompasses the following responsibilities and skills:

- Develop software multimedia components to tie hardware accelerated codecs or software codecs into standard multimedia frameworks.
- Implement new software protocols across customer designs through direct customer support, technical documentation, and online via support forums and open source communities.
- Thrive in a fast paced and dynamic environment where high-energy, prioritization skills and adaptability are a must!

Life at TI as a Software Engineering Intern: Enjoy an environment of smart collaboration with a global team of the industry's top engineering minds at your disposal. TI's corporate culture is one of competitive spirit which fuels continued business growth and constant career opportunities. While ethical business practice is a central aspect of the work place- you will be treated with respect, dignity, kindness and courtesy.

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Qualifications:

- Degree Type: Pursuing BS or MS EE or ECE
- GPA: 3.0/4.0 (minimum)
- Strong knowledge of software development in C++/C/Java.
- Experience debugging complex software systems and/or in implementing new software components/features.
- Demonstrated problem solving and communication skills

Additional Preferred Qualifications:

- Experience on open source projects and upstreaming contributions.
- Experience with GStreamer or other multimedia frameworks (i.e. DirectShow or OpenCore, Khronos OpenMAX IL).

In order to be eligible for this position you must:

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  - Create a new account,
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  - Update your profile (enter your contact information, work experience and education)
  - And apply to Job ID: 696BR
- 

Role: Software Engineer

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TITLE: Systems Engineering Intern & Systems Engineer

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#### Additional Preferred Qualifications

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